

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
)	
SEKIGUCHI et al.)	
)	
Application Number: To be Assigned)	
)	Art Unit 2818
Filed: Concurrently herewith)	
)	
For: SEMICONDUCTOR MEMORY)	
)	
Attorney Docket No. HITA.0480)	

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

INFORMATION DISCLOSURE STATEMENT

The above-referenced application is a Continuation of U.S. Serial No. 10/287,678 filed November 5, 2002. It includes the same disclosure as U.S. patent application Serial No. 10/287,678.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

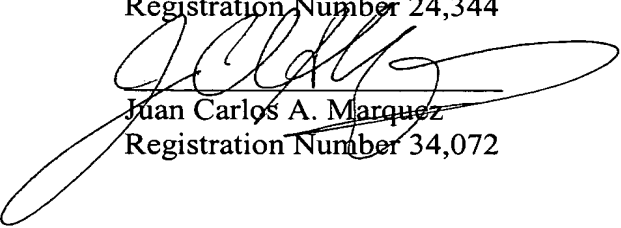
This Information Disclosure Statement is submitted with the above-captioned U.S. Continuation application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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Form PTO 1449 U.S. Department of commerce Patent and Trademark Office Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER	SERIAL NUMBER
	HITA.0470	To be assigned
	APPLICANT	
	SEKIGUCHI et al.	
	FILING DATE	GROUP
	Concurrently herewith	

U.S. Patent Documents

Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	XX	6,075,735	06/13/2000	Sugibayashi			10/01/1998
	X	5,050,126	09/17/1991	Tanaka et al.			10/03/1988
	X	5,371,711	12/06/1994	Nakayama, T.			03/03/1994
	X	6,414,893	07/02/2002	Miyamoto, J.			08/19/1998
	X	5,592,407	01/07/1997	Konishi et al.			02/23/1995

Foreign Patent Documents

Examiner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							YES	No
	XX	11-110967	10/01/97	Japan			Abstract	X
	XX	2002-93158	090/8/2000	Japan			Abstract	X

Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

XX	Satoshi Utsugi, Masami Hanyu, Yoshinori Muramatsu and Tadahiko Sugibayashi, "Noncomplimentary Rewriting and Serial-Data Coding Scheme for Shared-Sense-Amplifier Open-Bit-Line DRAM", IEEE Journal of Solid-State Circuits, Vol. 34, No. 10, October 1999, pp. 1391-1394		
<table border="1"> <tr> <td>EXAMINER</td> <td>DATE CONSIDERED</td> </tr> </table>		EXAMINER	DATE CONSIDERED
EXAMINER	DATE CONSIDERED		
<i>Examiner: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant</i>			

PTO 1449

X Cited by Examiner in Parent
XX Filed by applicant in parent